FACULTY PROFILE

Faculty Name: K SIVA RAMUDU

Faculty Photo:

Faculty Description:

K SIVA RAMUDU is the Professor, Department of Electrical and Electronics Engineering at Dr. K. V. Subba Reddy Institute of Technology, Kurnool.

Profile Tab:

Qualification

Qualification	Institution	Year
Bachelor's degree	Madanapalle Instituteof Technology & Sciences	2011
Master's degree	Annamacharya Institute of Technology & Sciences	2014

Experience

Designation	Institution	From	To
Assistant Professor	Golden Valley Integrated Campus	2013	2015
Assistant Professor	Brindavan Institute of Technology & Science (BITS)	2015	2019
Assistant Professor	Dr KV Subba Reddy Institute of Technology	2019	till date

Responsibilities Tab:

- Department Exam section Incharge

- Class Teacher
- Mentor Incharge

Teaching Tab:

Subjects handled

- Power Electronics
- Electrical Machines
- Electrical Circuits
- Basic Electrical Engineering
- MATLAB in Power electronics

Research Tab:

- Power systems
- Power Electronics

Publications tab:

- K. Siva Ramudu, S. Naveed Ur Rahman, S. Khaleel Basha "Less Power Loss and More Reliability of Radial Distribution System by Placing of Distributed Generator UsingFuzzy Approach" International Journal of Scientific Progress and Research (IJSPR), ISSN: 2349-4689 Volume 22, Number 02, 2016.
- K. Siva Ramudu, K. Mounika, B. SreeRamulu"Impact of Distributed Generator for Loss Reduction and Improvement in Reliability of Distributed System" International Journal of Advanced Engineering, Management and Science (IJAEMS), [Vol-2, Issue-4, April- 2016], Infogain Publication (Infogainpublication.Com) ISSN:2454-1311.
- Kumar. D, K. Siva Ramudu, P. Naga Prasad "Optimal Location of UPFC By Using Modified Particle Swarm Optimization for Voltage Stability" The International Journal of Science & Technology (ISSN 2321 – 919x), Vol 2 Issue 11 October,2014.
- K. Siva Ramudu, Dr. M. Padma Lalitha, P. Suresh Babu "Optimal Placement of DG for Loss Reduction and Voltage Sag Mitigation in Radial Distribution Systems Using ABC Algorithm" ACEEE Int. J. On Electrical and Power Engineering, Vol. 5, No. 1, February 2014.
- K. Siva Ramudu, M. Padma Lalitha, P. SureshBabu "Siting and Sizing of DG for Loss Reduction and Voltage Sag Mitigation in RDS Using ABC Algorithm" International Journal of Electrical and Computer Engineering (IJECE), Pp. 814~822, ISSN: 2088-8708,Vol. 3, No. 6, December2013.
- K. Siva Ramudu, M. Padma Lalitha, P. Suresh Babu "Siting and Sizing of DG for Loss Reduction and Voltage Sag Mitigation in RDS Using ABC Algorithm" Proceedings of 10th IRAJ International Conference, 27th October 2013, Tirupati, India. ISBN: 978-93- 82702-36-8.
- B. Prudvi kumar Reddy, K.Sivaramudu, A. Ramesh, "Controlling the Current in a Small-Scale DC Microgrid requires the use of a multi-level converter", Journal of Nonlinear Analysis and

Optimization: Theory & Applications Vol. 13, Issue. 2, 2022.

- K.Sivaramudu, B.Prudvi Kumar Reddy, K.Rajesh, "MODELLING AND CONTROL OF RURAL PV MICRO GRID USING FUZZY LOGIC CONTROLLER", in POSITIF Journal year Vol 21, Issue 6, 2021-22.

FDPs and STTPs Tab :

1.Participated in the Training programme/FDP On "Art Of Writing Research paper methods & solution (Advance Tools and Techniques for Research methodology) Organised by Saga University-Indore, Research Foundation Of India & RFI-CARE from 24 September to 30 September 2022

2. Participated in AICTE sponsored online Short Term Training Programme (STTP) on Application of Machine Learning and Artificial Intelligence Techniques for Control of Future Grid organized by the Department of Electrical &Electronics Engineering, NMAM Institute of Technology, Nitte during02-07August2021.

3.Participated and successfully completed the AICTE Sponsored Six day online Short Term Training Program (STTP) on Novel Design & Control Strategies and Innovative Technical Practices in LV/HV Modern Switchgear from 23.07.2020 to 29.07.2020 organised by the Department of Electrical and Electronics Engineering, Saranathan college of Engineering.

Blog Tab:

Blog link: **sivaram.1810@gmail.com**